

## Description

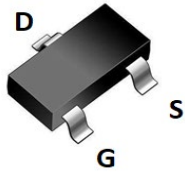
### JMT N-channel Enhancement Mode Power MOSFET

#### Features

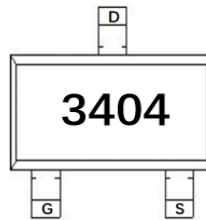
- 30V, 5.8A  
 $R_{DS(ON)} < 24m\Omega @ V_{GS} = 10V$   
 $R_{DS(ON)} < 33m\Omega @ V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead Free

#### Applications

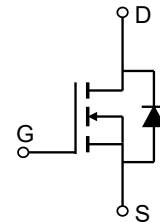
- Load Switch
- PWM Application
- Power Management



SOT-23 Top View



Marking and Pin Assignment



Schematic Diagram

## Package Marking and Ordering Information

Device Marking	Device	Outline	Package	Reel Size	Reel(pcs)	Per Carton (pcs)
3404	JMTL3404A	TAPING	SOT-23	7"	3000	120000

## Absolute Maximum Ratings (@ $T_A = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-to-Source Voltage	30	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ C$	5.8
		$T_A = 100^\circ C$	4
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	23	A
$P_D$	Power Dissipation	$T_A = 25^\circ C$	1.1
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>(3)</sup>	113	$^\circ C/W$
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ C$



## Electrical Characteristics (T<sub>J</sub> = 25°C unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	30	-	-	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1.0	μA
I <sub>GSS</sub>	Gate-Body Leakage Current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.2	1.7	2.2	V
R <sub>DS(ON)</sub>	Static Drain-Source ON-Resistance <sup>(4)</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5.5A	-	18.4	24	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 4.5A	-	25.5	33	mΩ
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 15V, f = 1MHz	-	485	-	pF
C <sub>oss</sub>	Output Capacitance		-	69	-	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		-	53	-	pF
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 0 to 10V V <sub>DD</sub> = 15V, I <sub>D</sub> = 5A	-	10	-	nC
Q <sub>gs</sub>	Gate Source Charge		-	2	-	nC
Q <sub>gd</sub>	Gate Drain("Miller") Charge		-	2	-	nC
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 15V I <sub>D</sub> = 5A, R <sub>GEN</sub> = 3Ω	-	4	-	ns
t <sub>r</sub>	Turn-On Rise Time		-	11	-	ns
t <sub>d(off)</sub>	Turn-Off DelayTime		-	14	-	ns
t <sub>f</sub>	Turn-Off Fall Time		-	2	-	ns
<b>Drain-Source Diode Characteristics and Max Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		-	-	5.8	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		-	-	23	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 5.8A	-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> = 5A, di/dt = 100A/μs	-	7.5	-	ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	2	-	nC

- Notes:
1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature.
  2. R<sub>θJA</sub> is measured with the device mounted on a 1inch<sup>2</sup> pad of 2oz copper FR4 PCB
  3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 0.5%.

## Typical Performance Characteristics

Figure 1: Output Characteristics

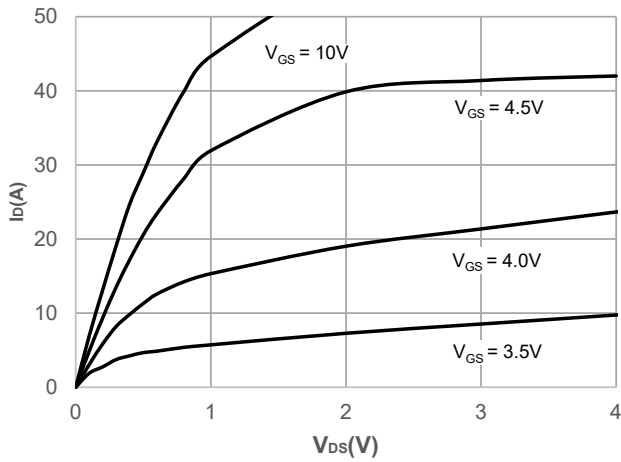


Figure 2: Typical Transfer Characteristics

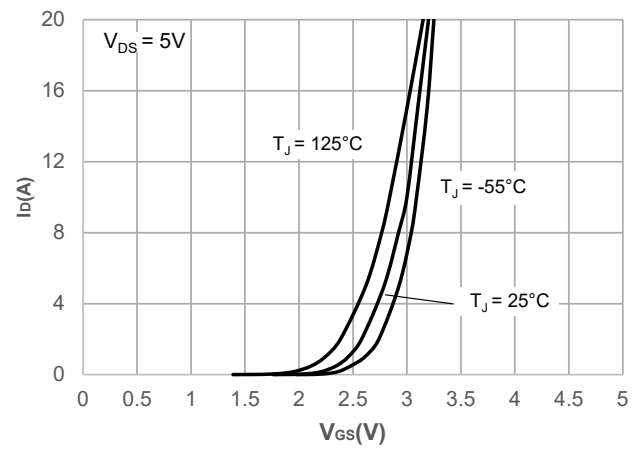


Figure 3: On-resistance vs. Drain Current

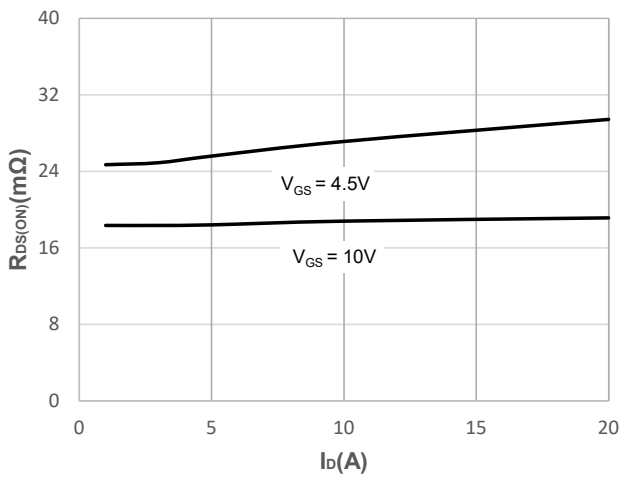


Figure 4: Body Diode Characteristics

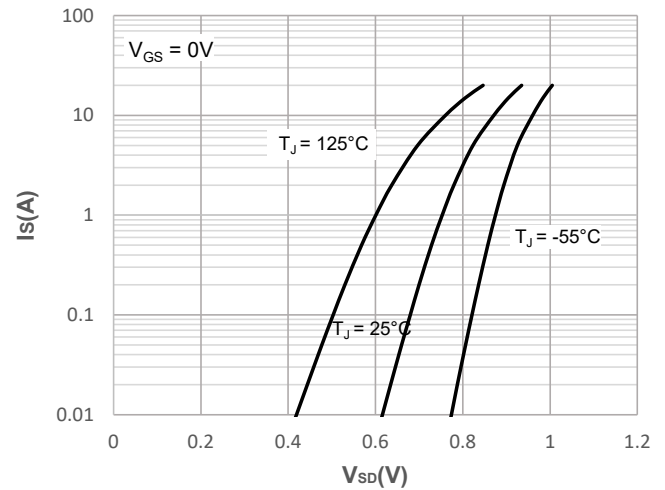


Figure 5: Gate Charge Characteristics

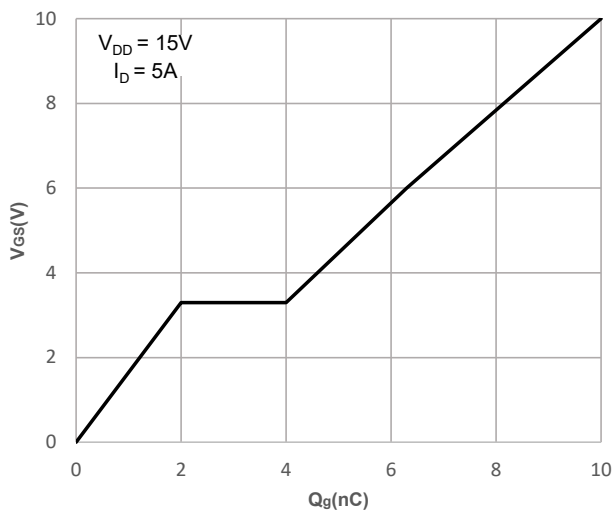
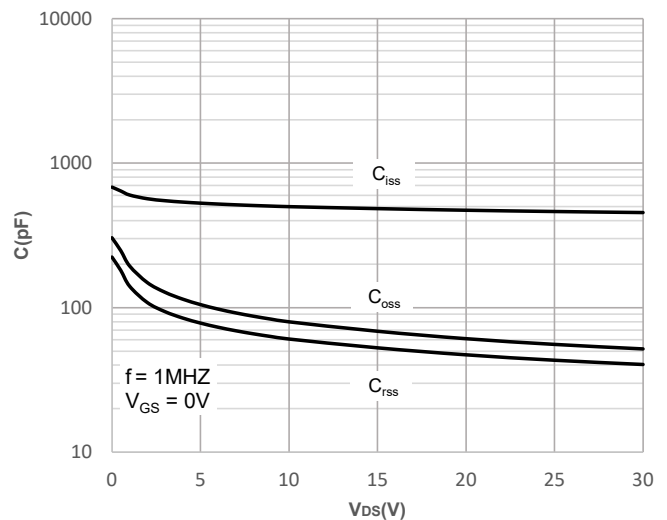


Figure 6: Capacitance Characteristics



## Typical Performance Characteristics

Figure 7: Normalized Breakdown voltage vs. Junction Temperature

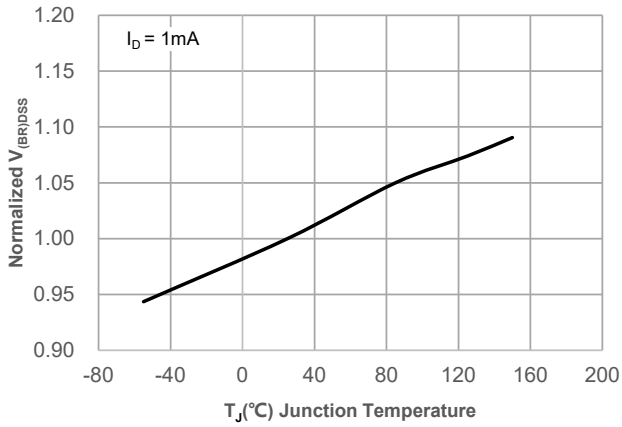


Figure 8: Normalized on Resistance vs. Junction Temperature

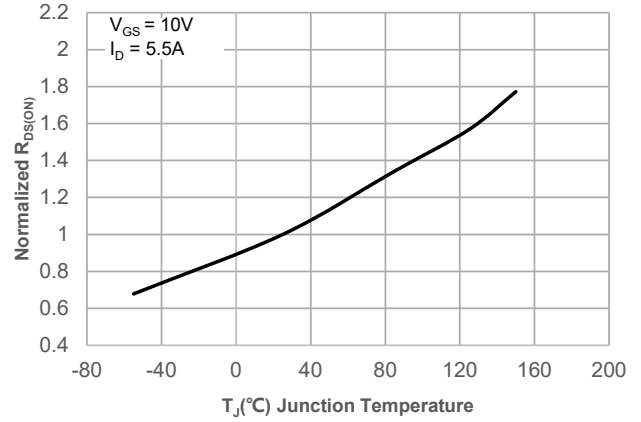


Figure 9: Maximum Safe Operating Area

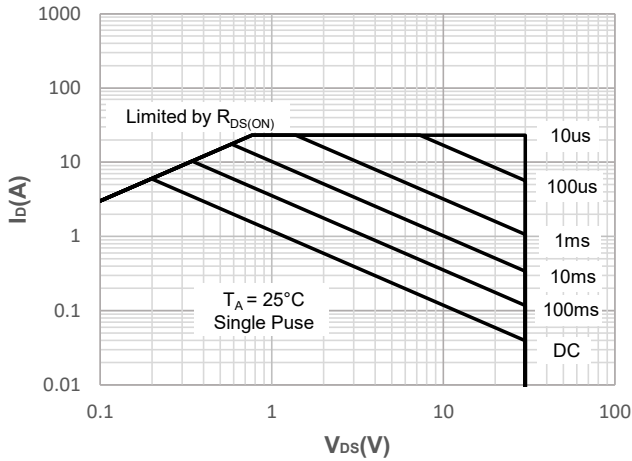


Figure 10: Maximum Continuous Driand Current vs. Ambient Temperature

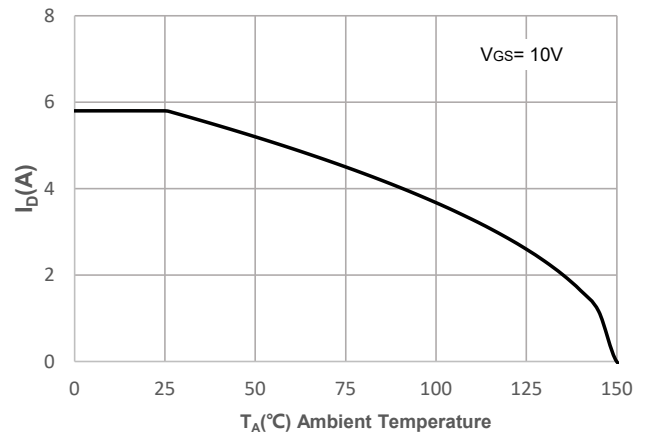


Figure 11: Normalized Maximum Transient Thermal Impedance

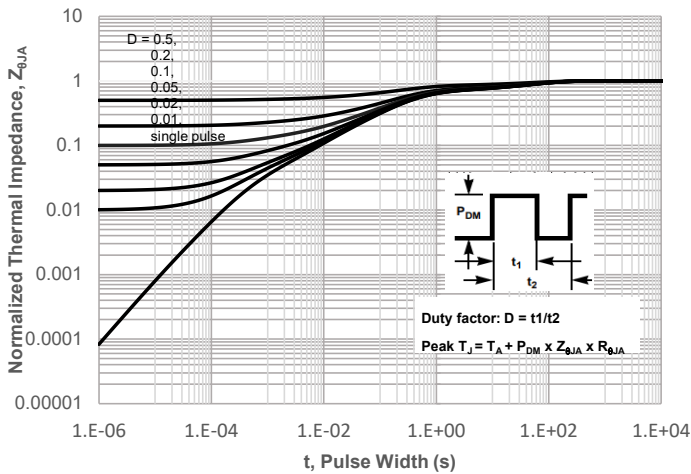
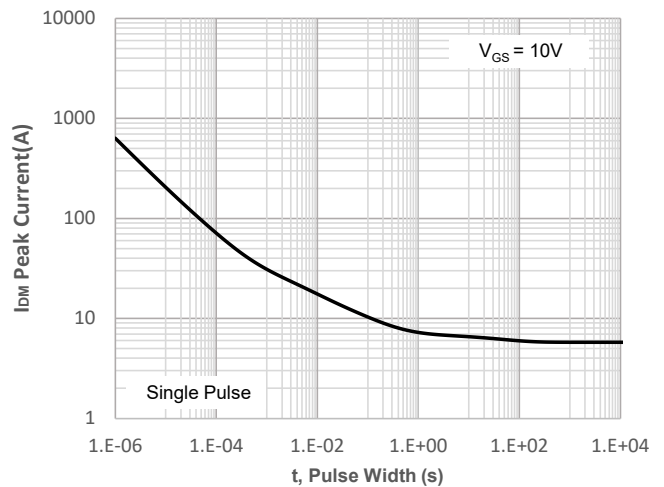


Figure 12: Peak Current Capacity



## Test Circuit

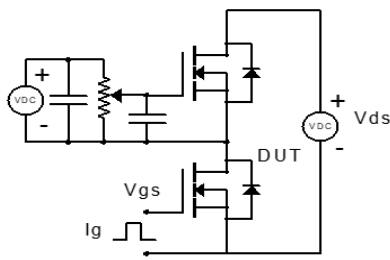


Figure 1: Gate Charge Test Circuit & Waveform

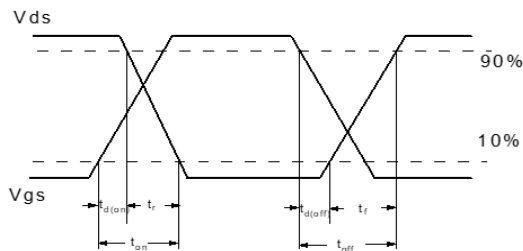


Figure 2: Resistive Switching Test Circuit & Waveform

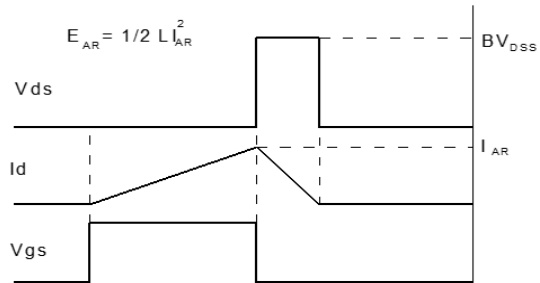
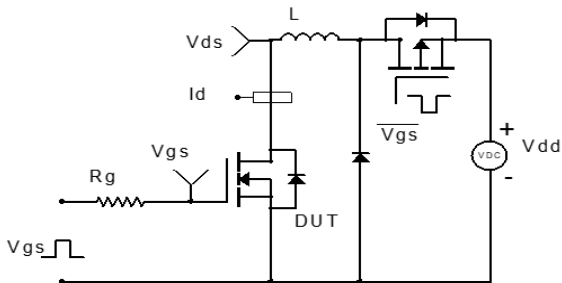


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

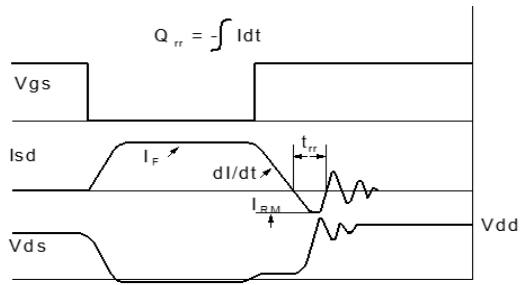
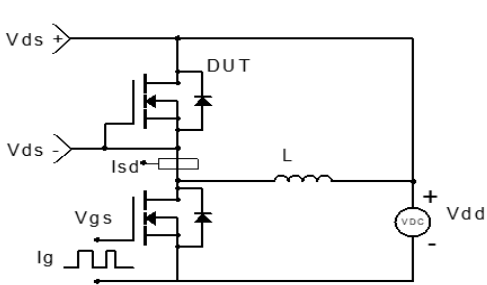
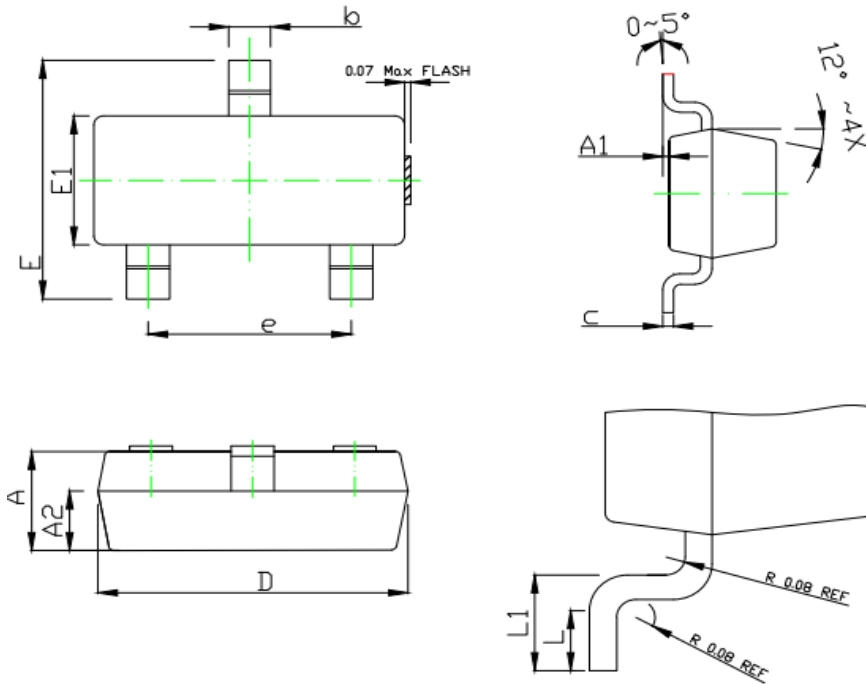


Figure 4: Diode Recovery Test Circuit & Waveform

## Package Mechanical Data(SOT-23)



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.95	1.00	1.05
A1	0.01	0.05	0.10
b	0.35	0.40	0.45
c	0.11 BSC		
D	2.80	2.90	3.00
B	2.30	2.40	2.50
E1	1.20	1.30	1.40
e	1.90 BSC		
L	0.20	-	-
L1	0.30	0.40	0.50
A2	0.60 REF		

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